Date:	Examination No.:	Version:2023-2024	Start: 03/09/2023
Module Name - Code	Digital system design		
Module Language:	English		
Responsible:	Nuraddin taha huseen		
Lecture (s):	Nuraddin taha huseen		
College:	College of Engineering – Salahaddin University-Erbil		
Duration:	15 week – 1 semester		
Course outcomes:	The purpose of this course is to learn the numbering system and the identification of logic gates and to study ways to design logical circuits using these gates and a study of commonly used services such as adder, subtractor, multiplexer and de multiplexer And finally identify the flip-flops and their use in circuits such as logical registers and counters		
Course Content:	1. logic families 2. C-Mos logic circuits 3. Semiconductor Memories 4. PLD		
Literature:	• S.N.Ali, Digital Electronics-Circuits,Systems • A.P Godes Digital design		
Type of Teaching:	3 hrs. in lectures "Theoretical"		
Pre-requisites:	None		
Preparation Modules:			
Frequency:	Fall Semester		
Requirements for credit points:	For the award of credit points, it is necessary to pass the module exam. It contains: More than one examination during the academic semester, Assignments and Final examination. **Student's attendance is required in all classes**.		
Credit point:	5		

Grade Distribution:	The following grade system is used for the evaluation of the module exam: The module exam is based on the summation of two categories of evaluations: **First: (40%)** of the mark is based on the academic semester effort which includes - More than one examination during the academic semester and assignments 15% - Midterm exam 20% Practical part 15% **Second: (40%)** of the mark is based on final examination that is comprehensive for the whole of the study materials reviewed during the academic semester. 10% practical final examination
Work load:	The workload is 45 hrs.